TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA2057N

#### AM / FM IF + FM St DET (for digital tuning system)

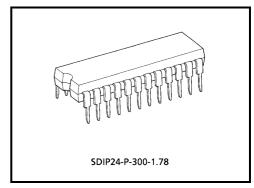
TA2057N is the AM / FM IF + FM St DET system IC, which is designed for DTS radios.

This IC is included many functions and these can be used for digital tuning system with IF counter.

#### Features

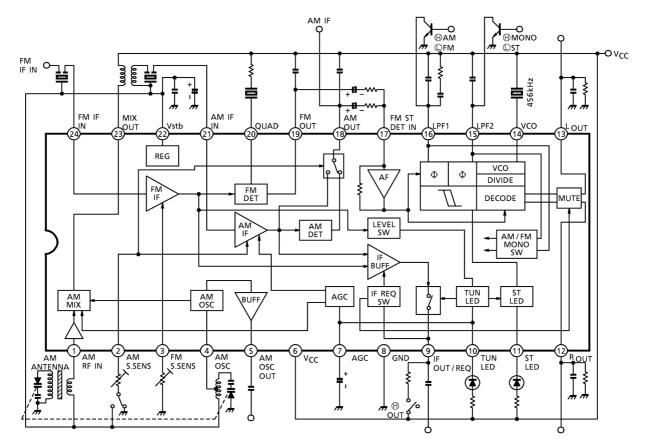
- Suitable for combination with digital tuning system which has IF counter.
- One terminal type AM / FM IF count output (auto stop signal) for IF counter of digitl tuning system.
- Built-in mute circuit for IF count output.
- Adjustable for IF count output sensitivity by external resistance of pin(2) (AM), and pin(3) (FM).
- For adopting ceramic discriminator and ceramic resonator, it is not necessary to adjust the FM quad detector circuit and FM st DET vco circuit.
- Built-in AM local oscillator buffer output circuit.
- Built-in AM IF buffer output circuit for AM stereo.
- Operating supply voltage range (Ta = 25°C): VCC = 3.5~14V

%Handle with care to prevent devices from deteriorations by static electricity.



Weight: 1.2g (typ.)

#### Block Diagram



# <u>TOSHIBA</u>

# Terminal Explanation (terminal voltage shows typical value at Ta = $25^{\circ}$ C, V<sub>CC</sub> = 5V, SW<sub>3</sub> = off, SW<sub>9</sub> = GND, and non–signal test circuit)

Pin	Characteristic	Internal Circuit		tage (V)
No.	Characteristic		FM	AM
1	AM RF in	Vstb22 Vstb22 Vstb22 Vstb22 Vstb22 Vcc Vcc Vcc Vcc Vcc Vcc Vcc V	1.95	1.95
2	AM s ⋅ sens • AM IF count output sensitivity adjust terminal • Bias terminal for AM IF / DET out switch circuit V <sub>2</sub> = GND →AM DET out (pin(19)) V <sub>2</sub> = Vstb →AM IF out (pin(19))	AM ST T T T T T T T T T T T T T T T T T T	0.5 (at SW <sub>9</sub> = Vstb1.45) R2 = 10kΩ	0.5 (at SW <sub>9</sub> = Vstb1.45) R2 = 10kΩ
3	FM s+sens • FM IF count output sensitivity adjust terminal	3 ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	0 R <sub>3</sub> = 10kΩ	0 R <sub>3</sub> = 10kΩ

#### TA2057N

Pin	Objects of the		DC Voltage (V)			
No.	Characteristic	Internal Circuit	FM	AM		
4	AM OSC	4 Vstb ALC ALC	1.95	1.95		
5	AM OSC out		5.0	4.75		
6	V <sub>CC</sub>	-	5.0	5.0		
7	AGC		0.01	0.01		
8	GND	_	0	0		
9	IF out / REQ • IF count output terminal • IF count output / FM st DET mute circuit control terminal $V_9 = V_{CC} \rightarrow on$ $V_9 = open \rightarrow off$	V <sub>CC</sub>	_	_		

Pin			DC Vol	DC Voltage (V)		
No.	Characteristic	Internal Circuit	FM	ÂM		
10	Tun LED		_			
11	St LED		_	_		
12	R-out		1.4	1.4		
13	L-out		1.4	1.4		
14	VCO		4.3	4.3		
15	LPF2 • LPF terminal for synchronous detector • VCO stop terminal V <sub>15</sub> = GND →VCO stop	DC AMP	3.6	3.6		

#### TA2057N

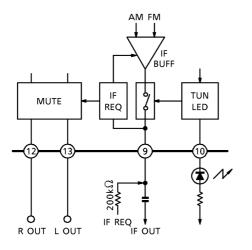
Pin	Objects of the		DC Vol	DC Voltage (V)			
No.	Characteristic	Internal Circuit	FM	AM			
16	LPF1 • LPF terminal for phase detector • Bias terminal for AM / FM switch circuit $V_{16} = GND \rightarrow AM$ $V_{16} = open \rightarrow FM$		3.6	0			
17	MPX in	1 τ τ τ τ τ τ τ τ τ τ τ τ τ	1.4	1.4			
18	AM DET out • AM DET / IF out $V_2 = GND \rightarrow AM DET out$ $V_2 = open \rightarrow FM IF out$	$V_{CC}$ b b c c c c c c c c	1.44	1.5 (at SW <sub>9</sub> = Vstb1.3)			
19	FM DET out	<sup>™</sup> <sup>™</sup> <sup>™</sup> <sup>™</sup>	1.2	1.5			

Pin No.	Characteristic	Internal Circuit	DC Voltage (V) FM AM		
20	QUAD		1.1	1.5	
21	AM IF in		1.95	1.95	
22	Vstb		1.95	1.95	
23	Mix out		5.0	5.0	
24	FM IF in	Vstb(22) Clower 24 (1) (2) (1) (2) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1	1.95	1.95	

# <u>TOSHIBA</u>

## **Application Note**

1. How to control the IF count output signal (pin(9) output)  $\$ 



		Tun	LED	
		On	Off	
Vg	Н	Come on	Non output	
¥9	Open	Non output	Non output	
			Circuit	
V <sub>9</sub>	Н	On		
٧g	Open	Off		

• Whether or not there is the IF count output signal (pin(9) output) is determined by the and of the pin(9) control voltage: V9 and tuning LED on / off switching.

In the condition of

V9: High (active high,  $V_{TH} = 2V$  (type.))

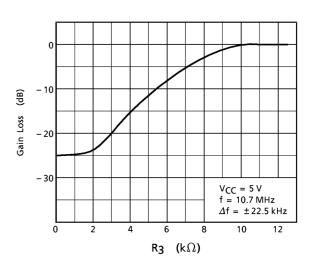
Tun LED: On (Vin  $\geq$  VL + 2dB (type.) )

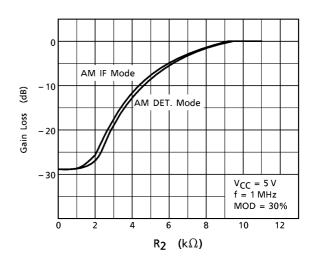
The IF count output signal come out from the pin(9).

- The signal waveform is FM: Rectangular, AM: Differential wave, and the level is  $500 mV_{p-p}$  (typ.)
- Mute circuit is action by the pin(9) control voltage: V9 (at high) in matrix output circuit.
- 2. How to adjust the IF count output sensitivity
- The IF count output sensitivity (search sensitivity) can be adjusted by varying the IF amp. Gain for FM and varying the mixer gain for AM.
- However, this is only possible at the auto-tuning mode (external voltage supplied to pin(9) is at high level). The original gain returns while receiving a broadcast station (supplied voltage to pin(9) is at low level).

• The gain loss of FM IF amp. (typ.)

R	3
0Ω	10kΩ
-25dB	0dB





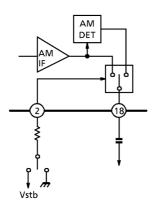
• The gain loss of AM mixer (typ.)

	R <sub>2</sub>		
	0Ω	10kΩ (Note)	
Det. Output mode	-29dB	0dB	
IF amp. Output mode	-29dB	0dB	

(Note): It is necessary to set up the value of  $R_2$  under  $20k\Omega$ .

When the  $\mathsf{R}_2$  is over  $20k\Omega,$  it is feared that the mode can not change.

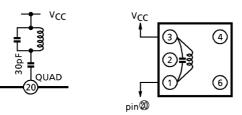
3. How to control the AM IF output signal (pin(18) output)



		Pin(18) Output Signal
V <sub>2</sub>	GND	AM DET out
v2	Vstb	AM IF out

- $4. \ AM \ local \ oscillator \ buffer \ output$
- The output impedance of AM local oscillator buffer output pin (pin(5) ) is  $750\Omega$  (typ.).
- It is possible to reduce the output level to add the resistance between the pin(5) and V<sub>CC</sub> line. The signal waveform is the rectangular wave, and the level is  $500mV_{p-p}$ . (fosc = 1.45MHz (typ.))
- The higher local oscillation frequency (fOSC) to be, the lower buff output level to be owing to the load capacity. So, in the case that it is connected to other circuits, take care of the input capacity of these circuits and stray capacity of wire.
- 5. FM detector circuit

For the FM detector circuit, detector coil is able to use instead of ceramic discriminator. Recommended circuit and recommended coil are as follows. In this case, please take care that  $V_{in}$  (lim.) fall a little.

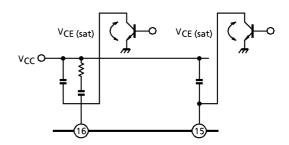


Test	Co			Tu	rns		Wire	555
Frequency	(pF)	Qo	1–2	2–3	1–3	4–6	(mmø)	REF.
10.7MHz	100	100	_	_	12	_	0.12 UEW	Sumida electric Co., ltd. 2153–4095–189 or equivalent

# <u>TOSHIBA</u>

- 6. FM / AM switch and forced monaural switch
- FM / AM switchover and stereo / forced monaural switchover are done by pin(16) and pin(15).
- FM / AM switch (pin(16) ) V16: Low (active low, VTH = 1.0V (typ.)  $\rightarrow$ AM V16: Open  $\rightarrow$ FM
- Stereo / forced monaural switch (pin(15) )
  - V15: Low (active low,  $V_{TH} = 1.0V$  (typ.)  $\rightarrow$ Forced manaural

V15: Open  $\rightarrow$ Stereo



## Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	14	V
LED current	I <sub>LED</sub>	10	mA
LED voltage	V <sub>LED</sub>	14	V
Power dissipation	P <sub>D</sub> (Note)	1200	mW
Operating temperature	T <sub>opr</sub>	-25~75	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

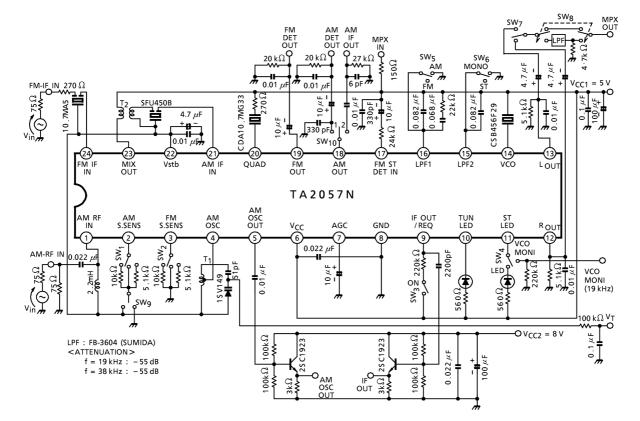
(Note): Derated above 25°C in the proportion of 9.6mW / °C

# Electrical Characteristics Unless Otherwise Specified, Ta = 25°C, $V_{CC1}$ = 5V, $SW_3$ = off, $SW_9$ = GND, $SW_{10}$ = 1 FM IF: f = 10.7MHz, $\Delta f$ = ±22.5kHz, f<sub>m</sub> = 1kHz AM: f = 1MHz, MOD = 30%, f<sub>m</sub> = 1kHz FM St Det: f<sub>m</sub> = 1kHz

	Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit	
Supply current		I <sub>CC</sub> (FM)	1	FM mode, V <sub>in</sub> = 0	—	20	26	mA	
		I <sub>CC</sub> (AM)	1	AM mode, V <sub>in</sub> = 0	—	20	26	ma	
	Input limiting voltage	V <sub>in (lim)</sub>	1	-3dB limiting point	38	43	48	dBµV EMF	
	Recovered output voltage	VOD	1	V <sub>in</sub> = 80dBµV EMF	70	100	140	mV <sub>rms</sub>	
	Signal to noise ratio	S / N	1	V <sub>in</sub> = 80dBµV EMF	_	70	_	dB	
FM	Total harmonic distortion	THD	1	V <sub>in</sub> = 80dBµV EMF	_	0.2	_	%	
IF	AM rejection ratio	AMR	1	V <sub>in</sub> = 80dBµV EMF	_	50	_	dB	
	LED on sensitivity	VL	1	I <sub>L</sub> = 1mA	43	48	53	dBµV EMF	
	IF count output voltage	V <sub>IF</sub> (FM)	1	SW <sub>3</sub> : On, V <sub>in</sub> = 80dBµV EMF	350	500	_	mV <sub>p-p</sub>	
	IF count output sensitivity	IF <sub>sens</sub> (FM)	1	SW <sub>3</sub> : On, SW <sub>1</sub> : 0Ω	—	73	_	dBµV EMF	
				SW <sub>3</sub> : On, SW <sub>1</sub> : 5.1kΩ	_	62	_		
	Gain	GV	1	V <sub>in</sub> = 23dBµV EMF	35	70	105	mV <sub>rms</sub>	
	Recovered output voltage	V <sub>OD</sub>	1	V <sub>in</sub> = 60dBµV EMF	70	100	140	mV <sub>rms</sub>	
	Signal to noise ratio	S / N	1	V <sub>in</sub> = 60dBµV EMF	—	45	_	dB	
	Total harmonic distortion	THD	1	V <sub>in</sub> = 60dBµV EMF	_	0.5	_	%	
	LED on sensitivity	VL	1	I <sub>L</sub> = 1mA	19	24	29	dBµV EMF	
AM	Local OSC buff	Vosc	1	f <sub>OSC</sub> = 1.45MHz	350	500	-	m\/	
	output voltage	(AM)	1	f <sub>OSC</sub> = 27MHz	_	500	-	mV <sub>p-p</sub>	
	AM IF output voltage for AM stereo	VIF (ST)	1	SW <sub>9</sub> : Vstb, MOD = 0% V <sub>in</sub> = 60dBµV EMF, SW <sub>10</sub> : 2	800	1100	1400	mV <sub>p-p</sub>	
	IF count output voltage	V <sub>IF</sub> (AM)	1	SW <sub>3</sub> : On, V <sub>in</sub> = 60dBµV EMF	350	500	_	mV <sub>p-p</sub>	
	IF count output	IFsens	1	SW <sub>3</sub> : On, SW <sub>2</sub> : 0Ω	_	54	_	dBµV EMF	
	sensitivity	(AM)		SW <sub>3</sub> : On, SW <sub>2</sub> : 5.1kΩ	_	30	-	ODH V LIVIE	

Characteristic			Symbol	Test Cir– cuit	Test Condition		Min.	Тур.	Max.	Unit	
	Max. Composite input voltage		V <sub>in max</sub> (stereo)	1	L + R = 90%, P = 10% THD = 3%, SW <sub>8</sub> →LPF: On		-	800	_	mV <sub>rms</sub>	
	Separation				L+R	f <sub>m</sub> = 100Hz	_	45	_	dB	
			Sep.	1	= $180 \text{mV}_{\text{rms}}$ P = $20 \text{mV}_{\text{rms}}$ SW <sub>8</sub> $\rightarrow$ LPF: On	f <sub>m</sub> = 1kHz	35	45	_		
						f <sub>m</sub> = 10kHz	_	45	_		
	Total harmonic distortion	Monaural	THD (mono)		V <sub>in</sub> = 200mV <sub>rms</sub> (n	-	0.05	_	%		
FM St		Stereo	THD (st)	1	L + R = 180mV <sub>rms</sub> P = 20mV <sub>rms</sub> SW <sub>8</sub> →LPF: On	-	0.05	_			
DET	Voltage gain		G <sub>V</sub> (FM ST DET)	1	V <sub>in</sub> = 200mV <sub>rms</sub> (mono)		-2	0	2	dB	
	Channel balance		С. В.	1	V <sub>in</sub> = 200mV <sub>rms</sub> (mono)		-2	0	2	dB	
	Stereo LED sensitivity	On	V <sub>L</sub> (ON)			_	10	18	mV <sub>rms</sub>		
		Off	V <sub>L</sub> (OFF)	1	Pilot input	3	8	_			
	Stereo LED hystersis		V <sub>H</sub>	1	To LED turn off from LED turn on		_	2	_	mV <sub>rms</sub>	
	Capture range		C. R.	1	P = 20mV <sub>rms</sub>			±1.3	—	%	
	Signal to noise ratio		S / N	1	V <sub>in</sub> = 200mV <sub>rms</sub> (mono)		—	78	—	dB	

#### Test Circuit1



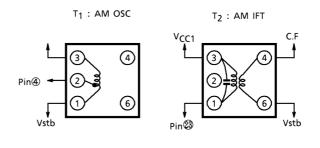
#### Coil Data (test condition1)

	f	L (µH)	C <sub>o</sub> (pF)	Qo		Τι	ırn		Wire (mmø)	Ref. (coil no.)
Coil No.					1–2	2–3	1–3	4–6		
T <sub>1</sub> AM OSC	796kHz	288	_	115	13	73	_	_	0.08 UEW	(S) 4147–1356–038 (T) A7BRS–12552Y (M) MJ–3273–3
T <sub>2</sub> AM IFT	455kHz	_	180	120	_	_	180	15	0.06 UEW	(S) 2150-2162-165 (T) 7MES-11368N (M) MJ-3337-1

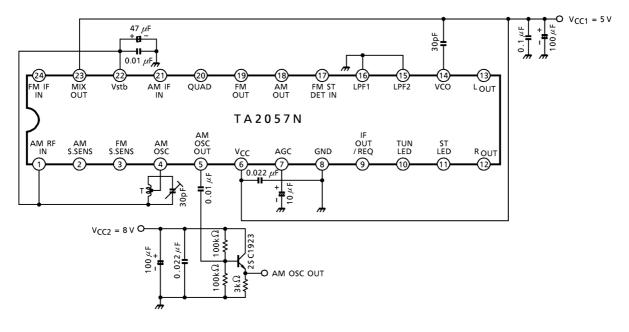
(S): Sumida electric co., ltd.

(T): Toko co., Itd.

(M): Mitsumi electric co., ltd.



#### **Test Circuit2**

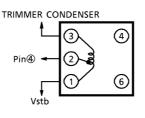


#### Coil Data (test circuit 2)

ſ	Coil No.	f	L (µH)	C <sub>o</sub> (pF)	Qo		Τι	ırn		Wire (mm∳)	Ref. (coil no.)
						1–2	2–3	1–3	4–6		
	T AM OSC	7.96MHz	1.4		84	1	6	7	_	0.08 UEW	(T) 7PL-1344Y

(T): Toko co., Itd.

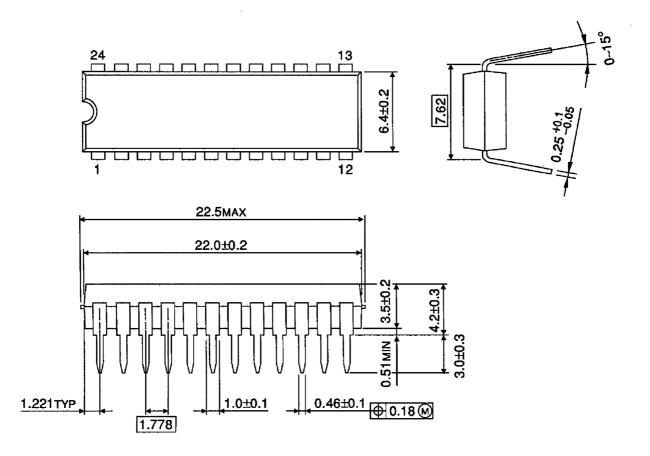
T: AM OSC



#### Package Dimensions

SDIP24-P-300-1.78

Unit : mm



Weight: 1.2g (typ.)

#### **RESTRICTIONS ON PRODUCT USE**

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.